

Fig. 1
PRIOR ART

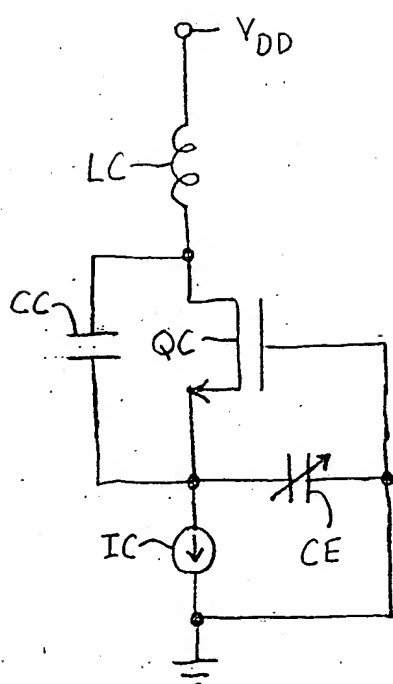
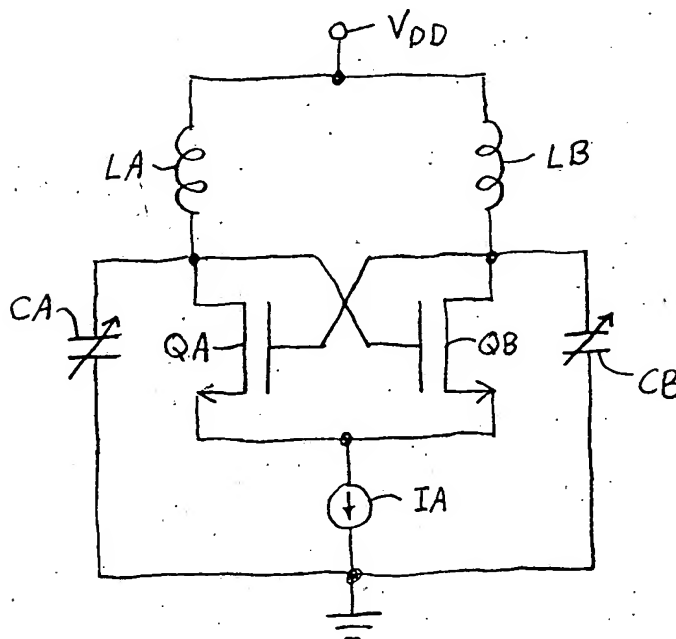


Fig. 2a PRIOR ART

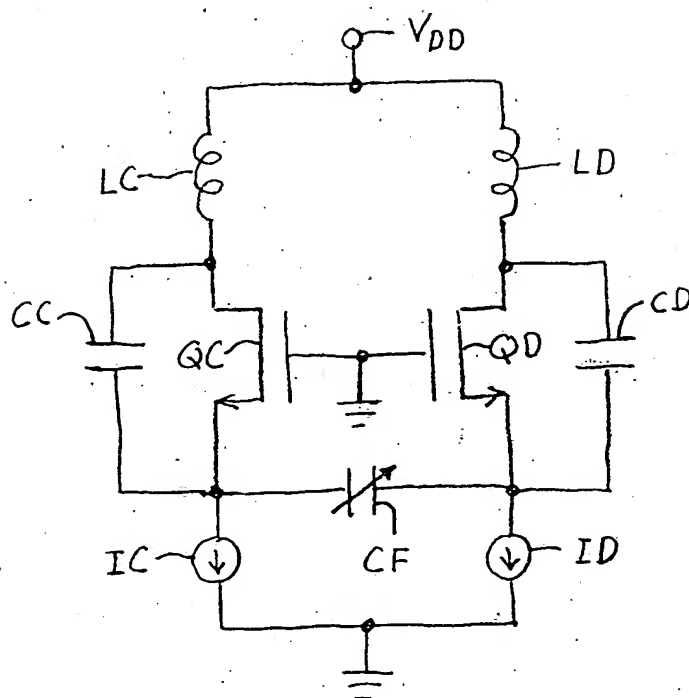


Fig. 2b PRIOR ART

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Fig. 3
PRIOR ART

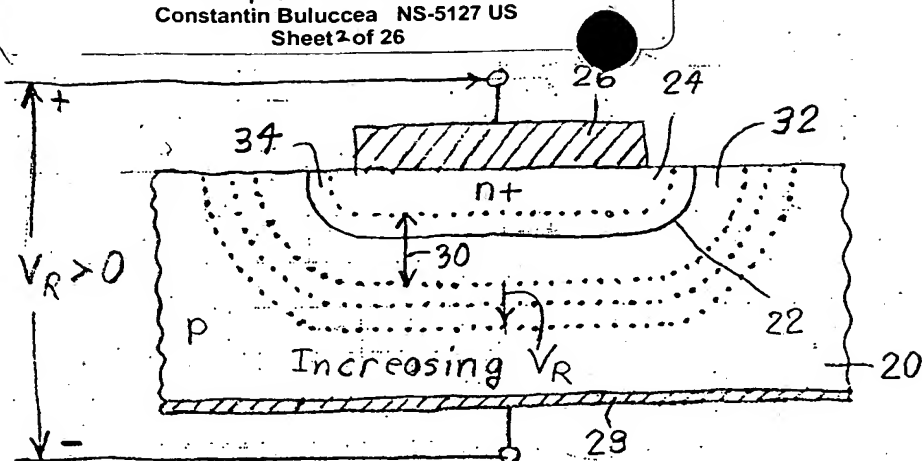


Fig. 4
PRIOR ART

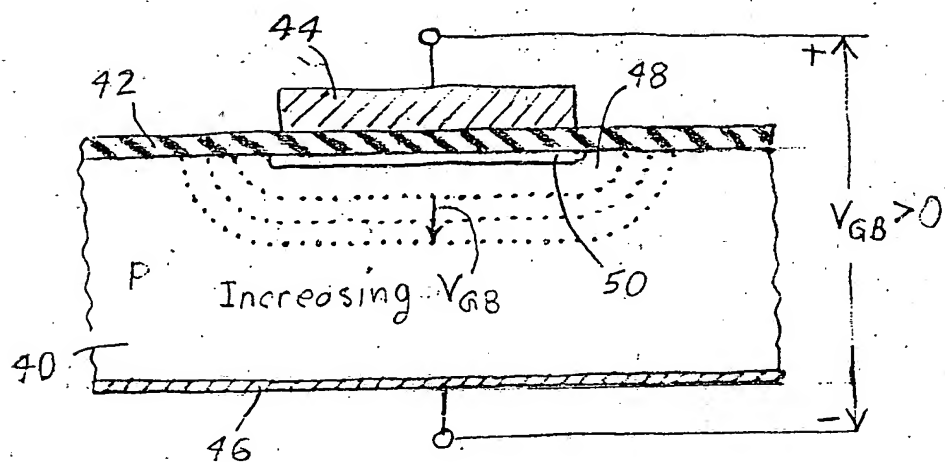
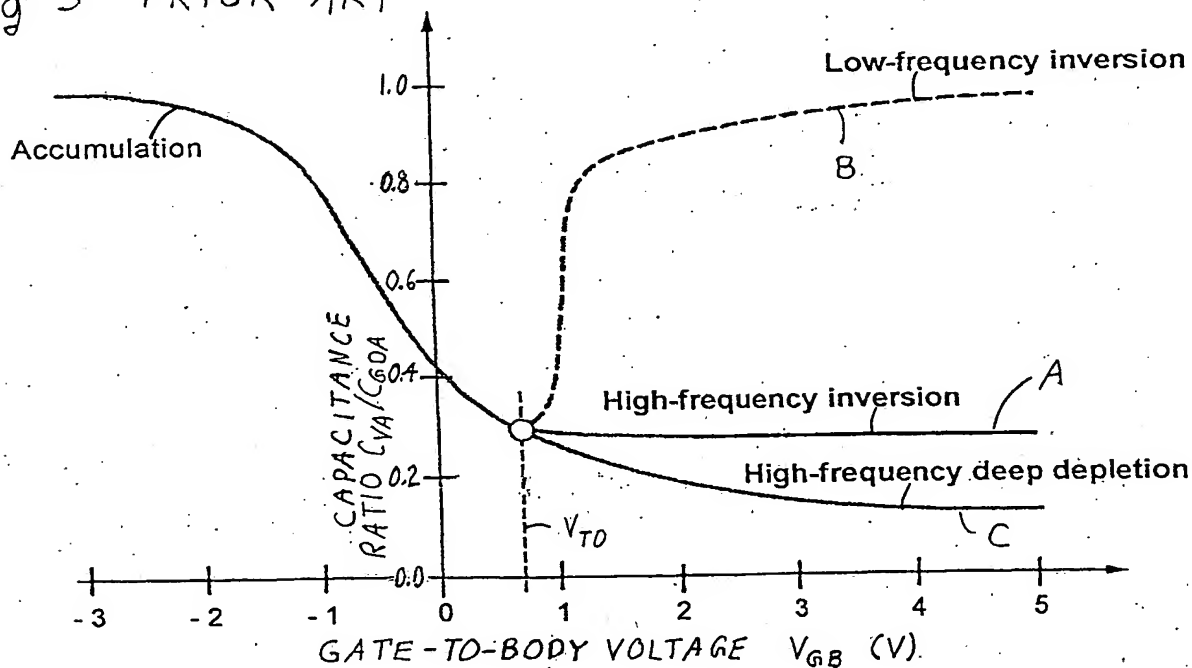


Fig 5 PRIOR ART



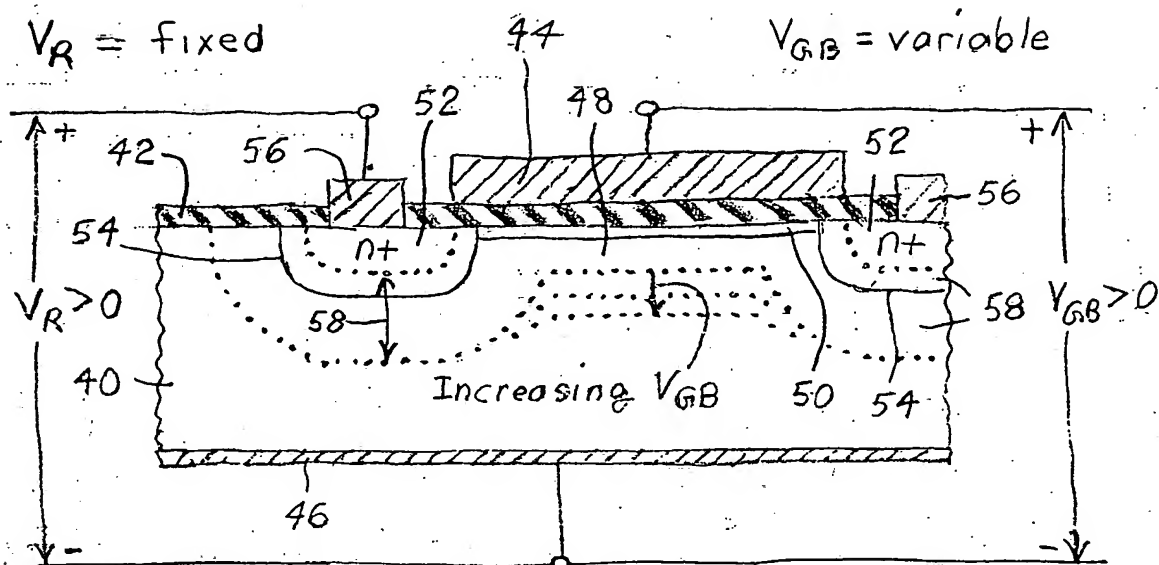


Fig. 6 PRIOR ART

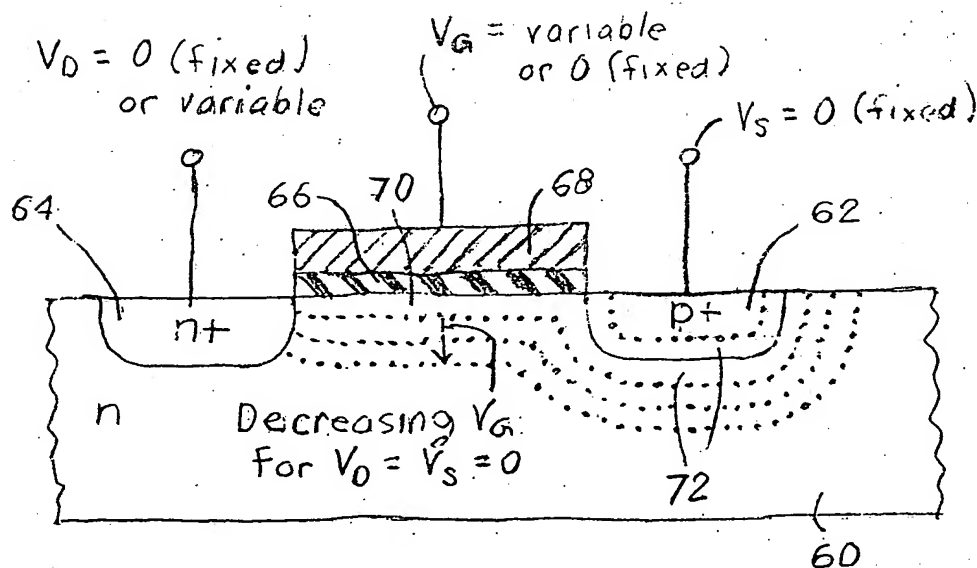
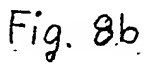
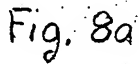
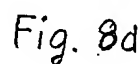
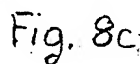


Fig. 7 PRIOR ART





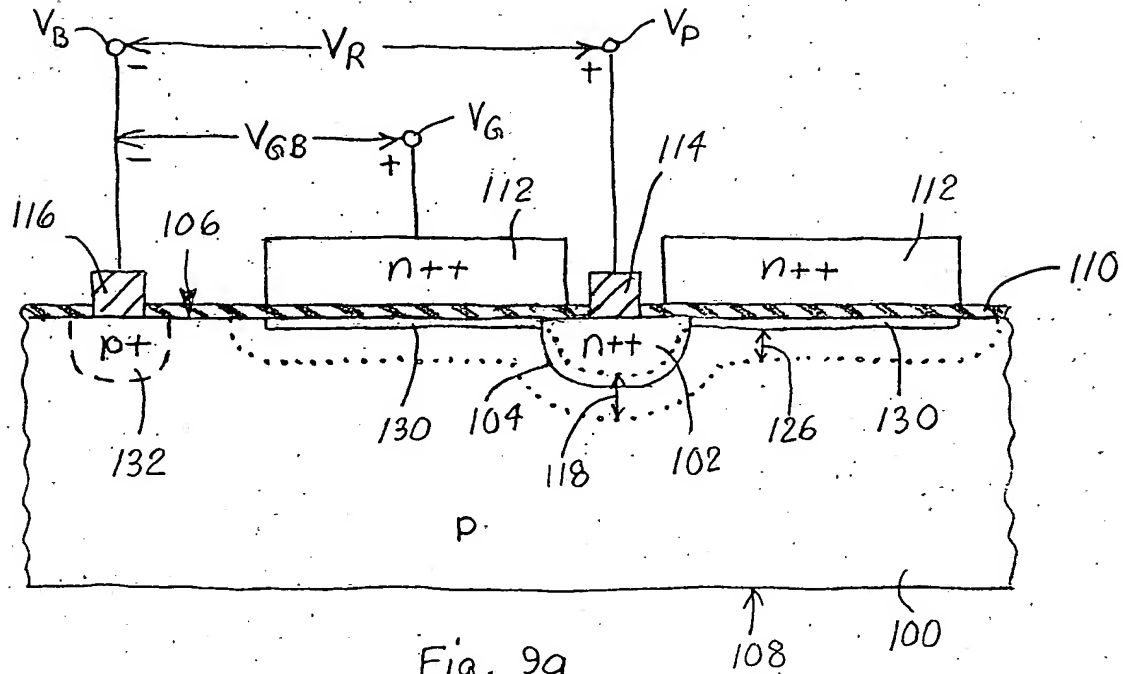
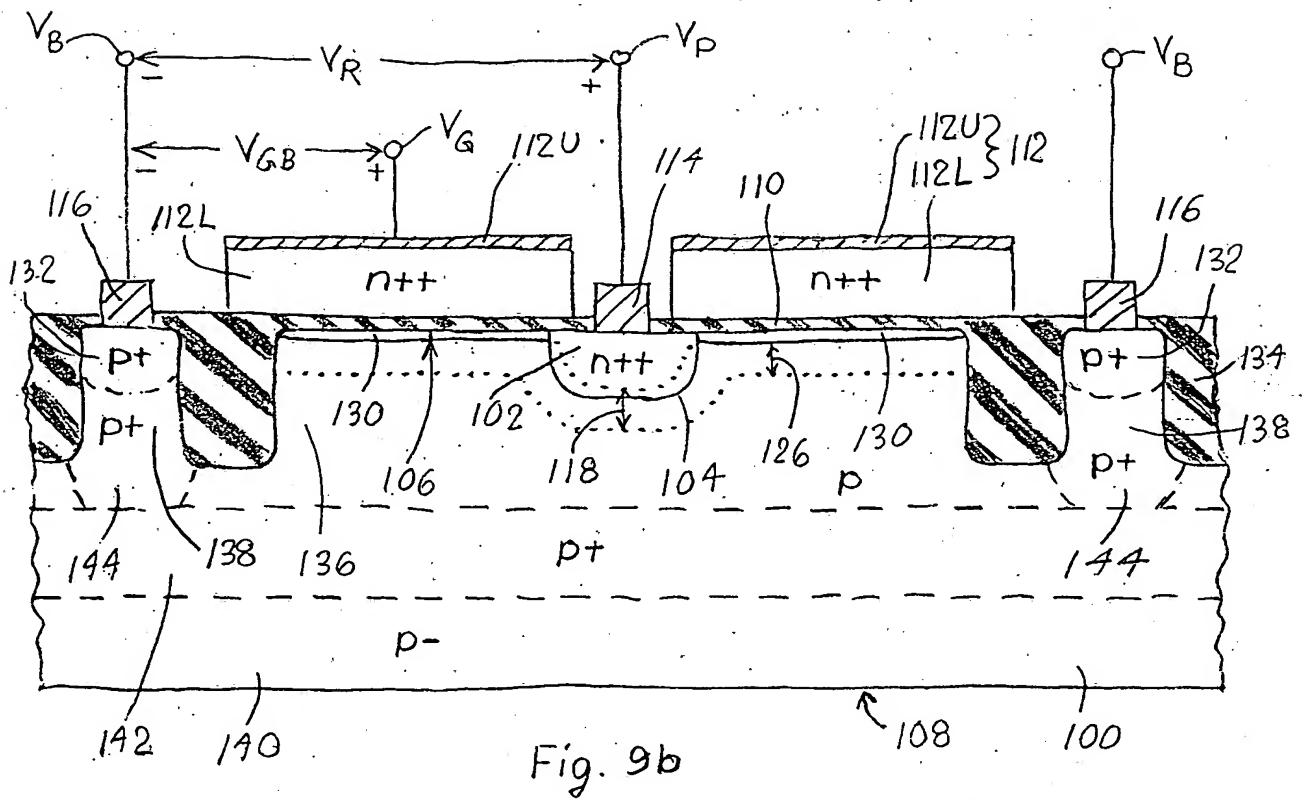
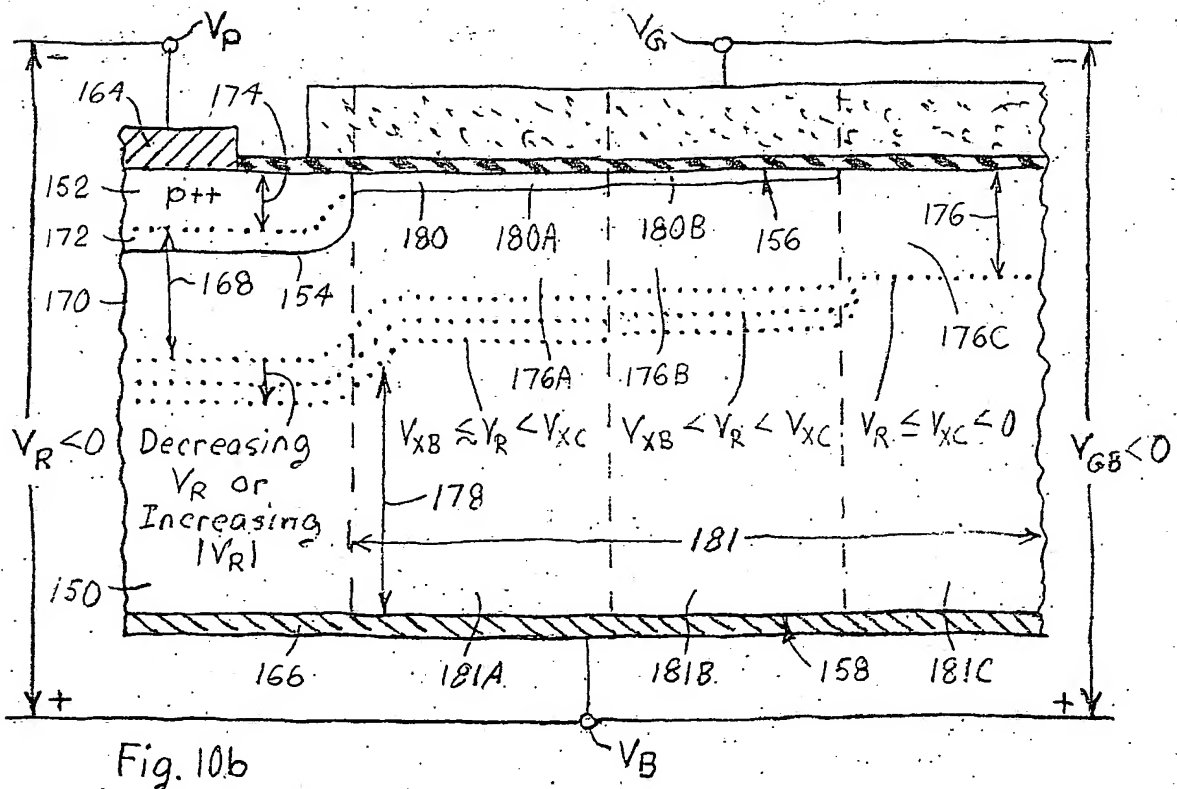
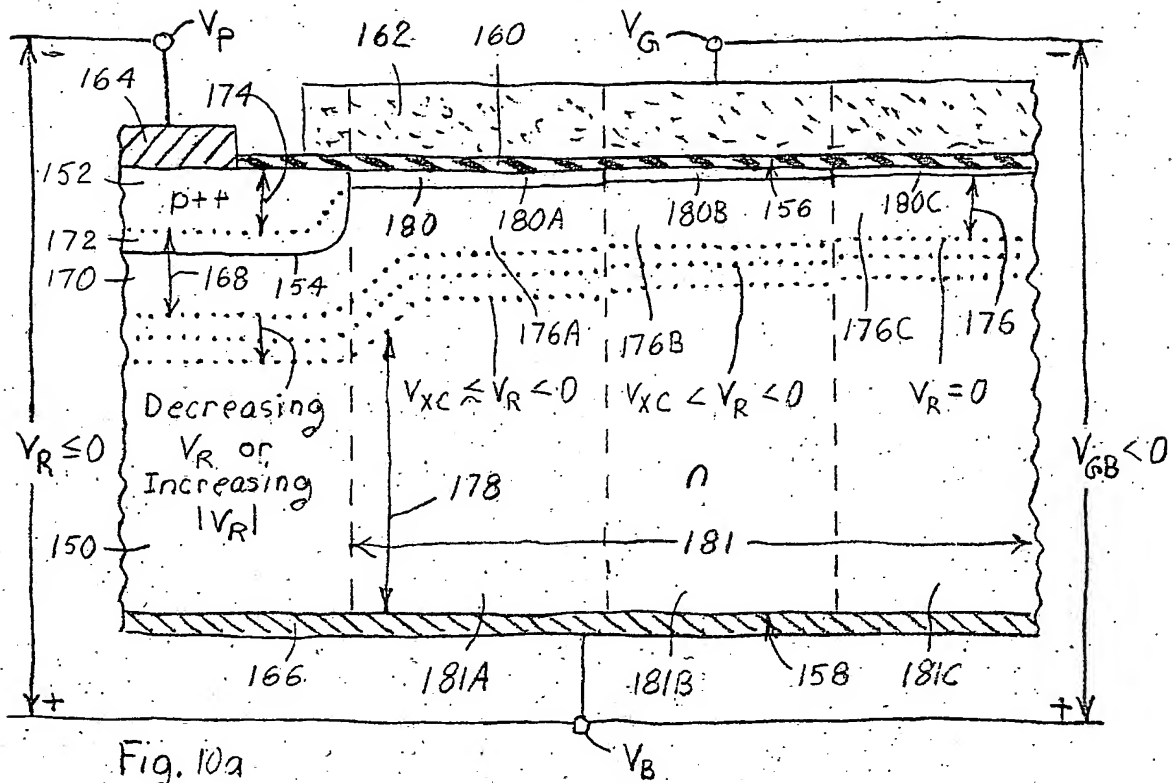
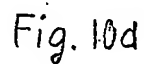
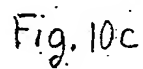


Fig. 9a







100-47689-1

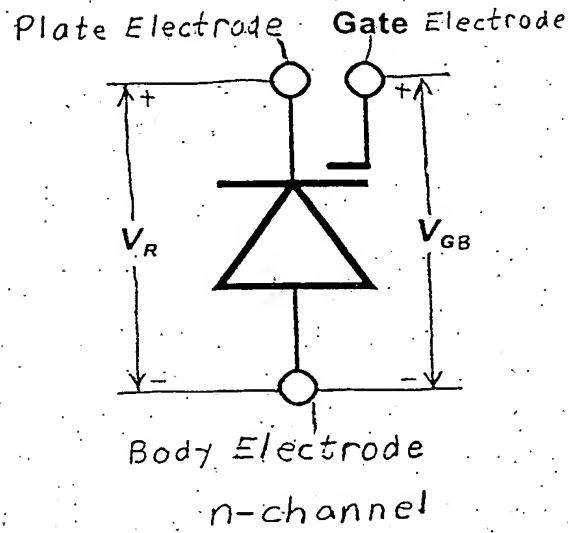


Fig. 11a

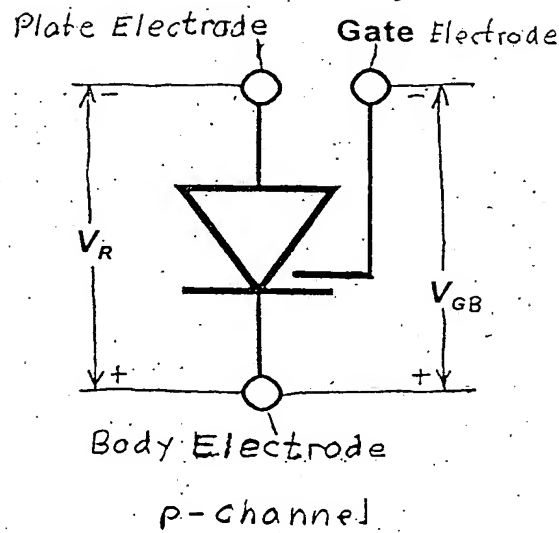
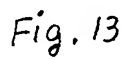
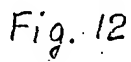
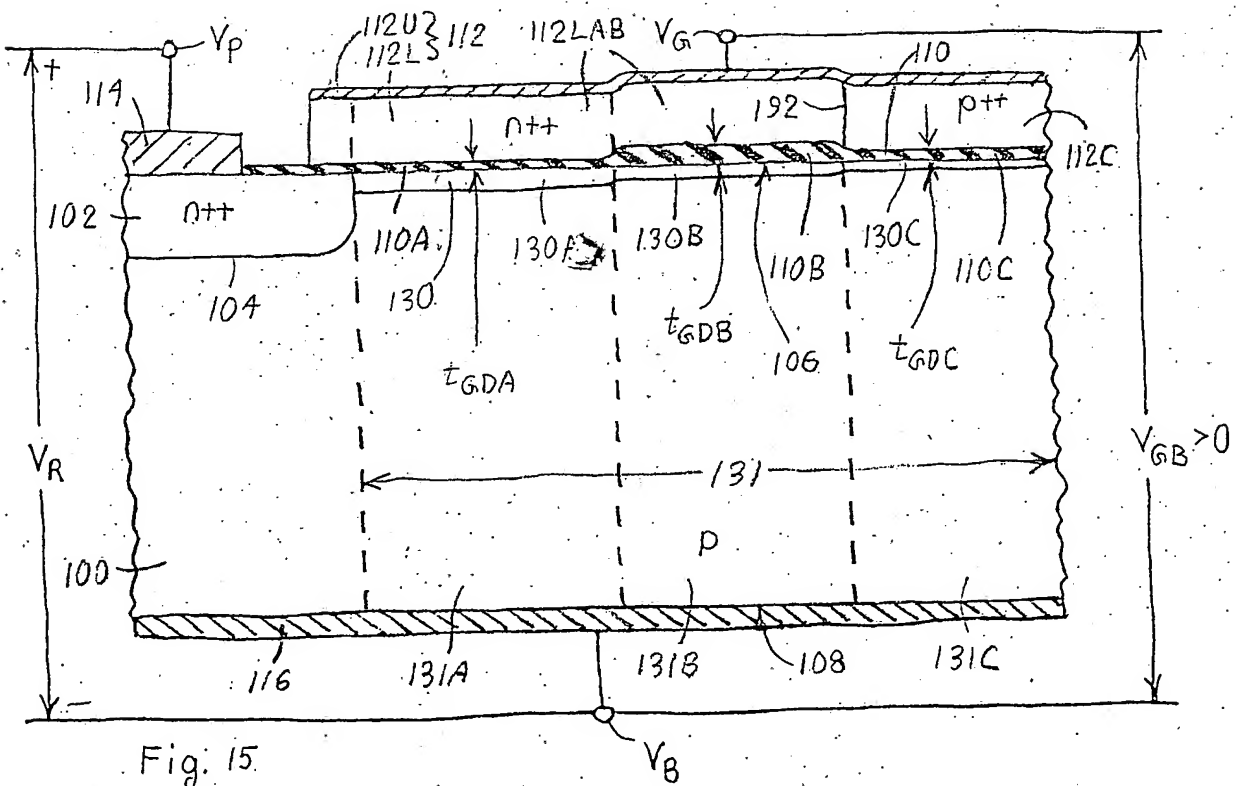
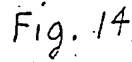


Fig. 11b

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[illegible]



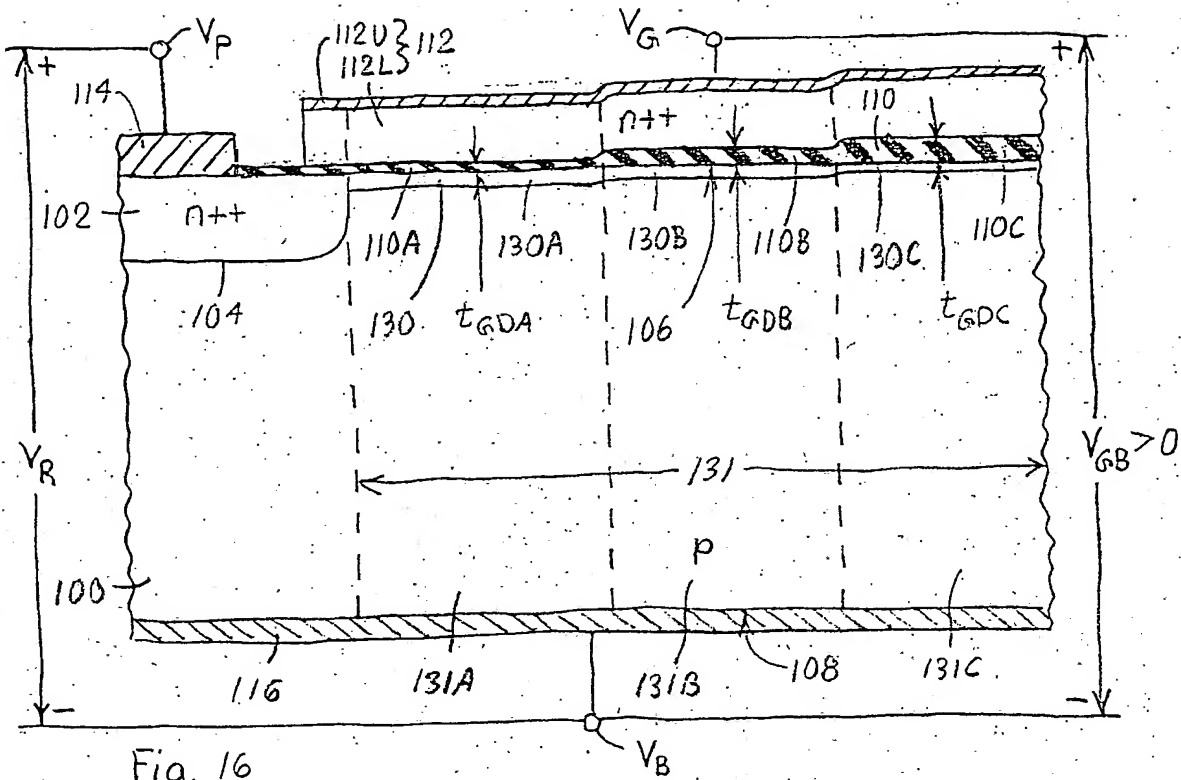


Fig. 16

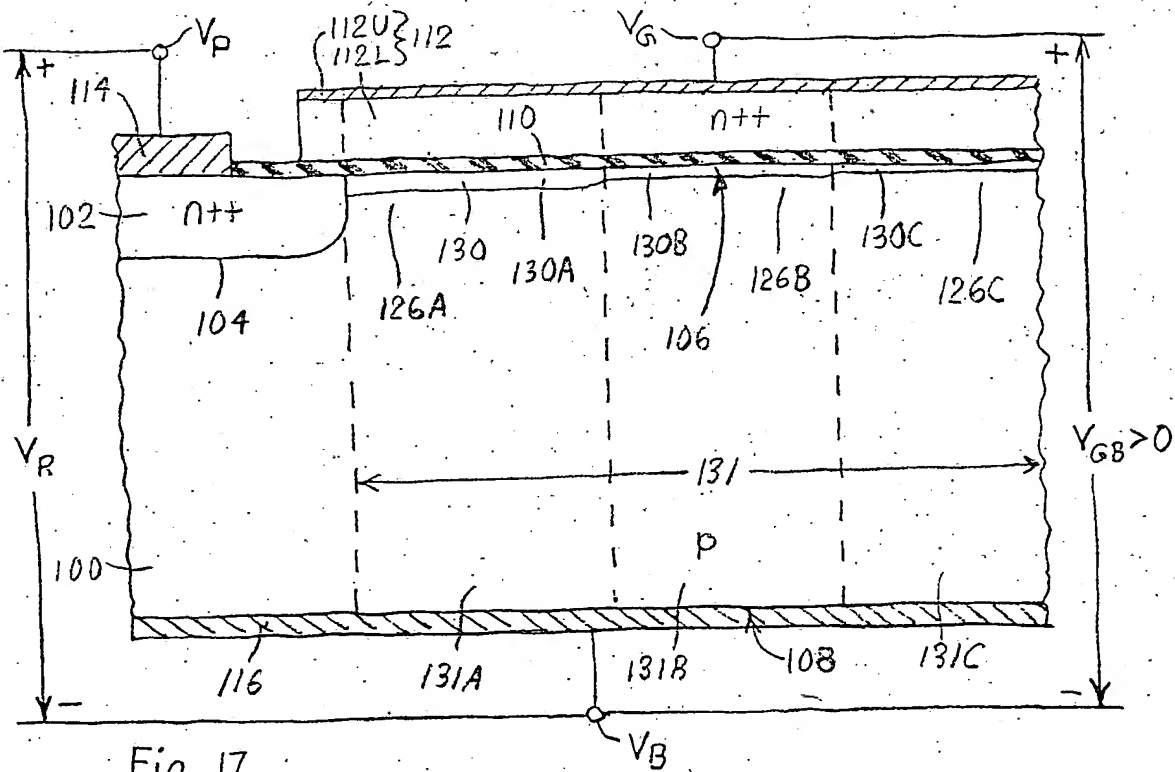
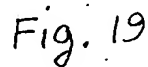
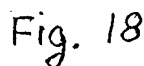


Fig. 17



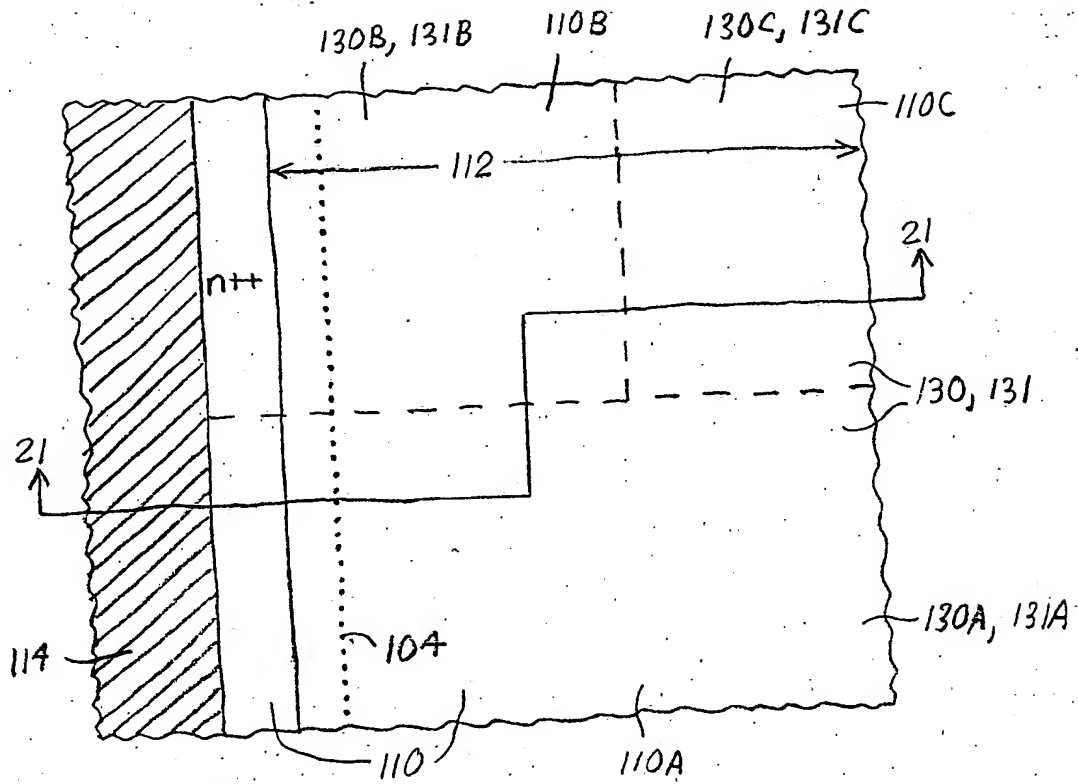


Fig. 20

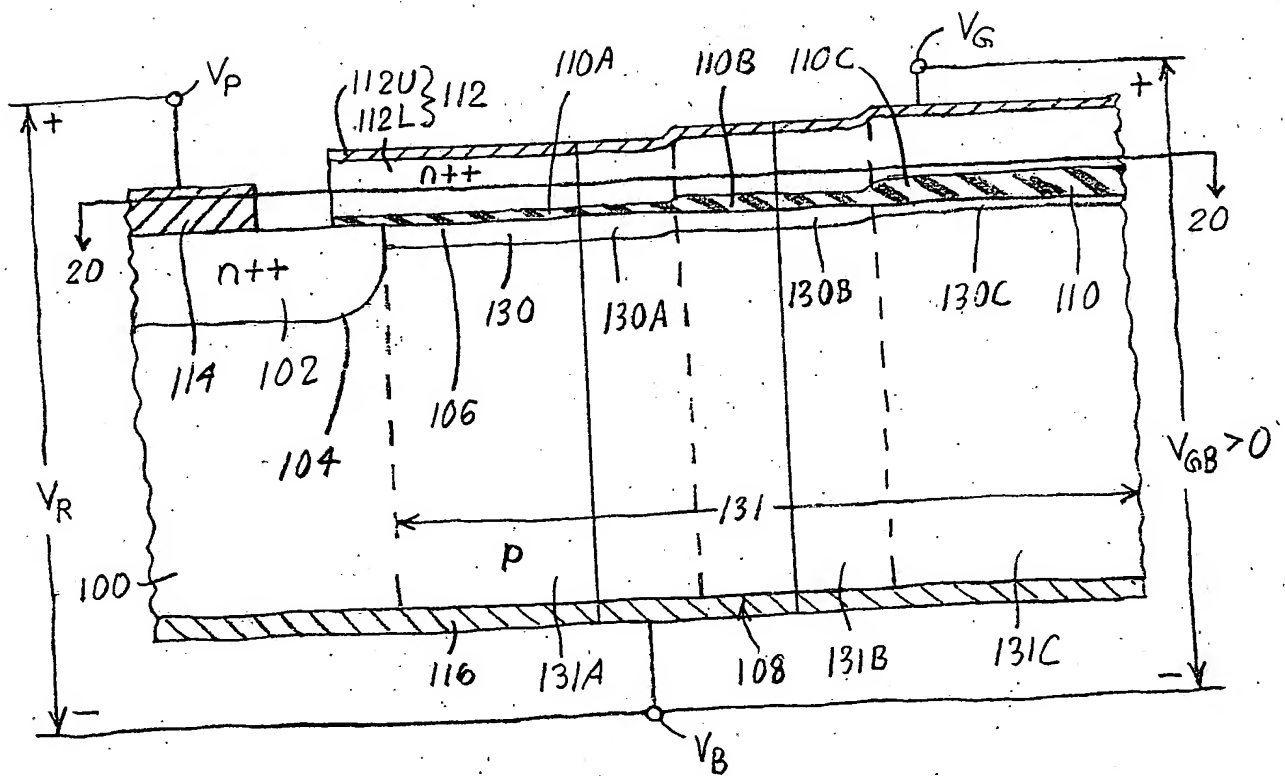
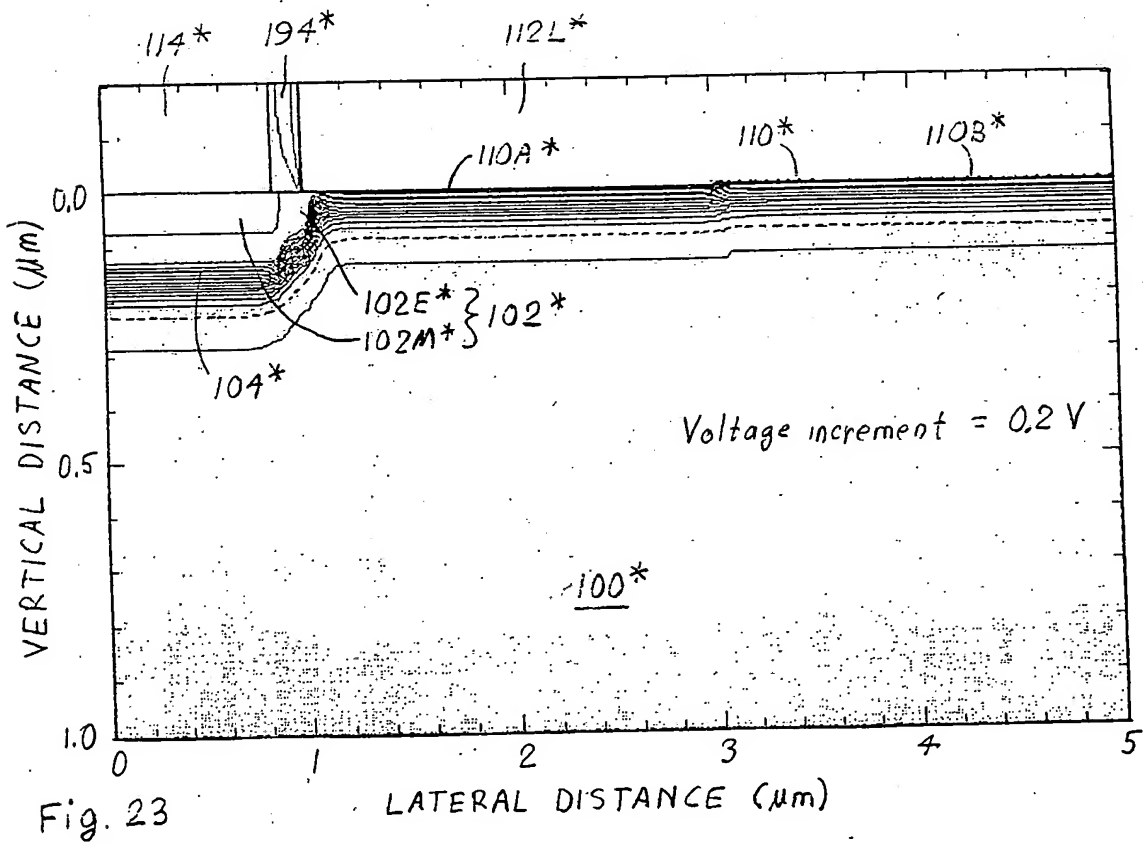
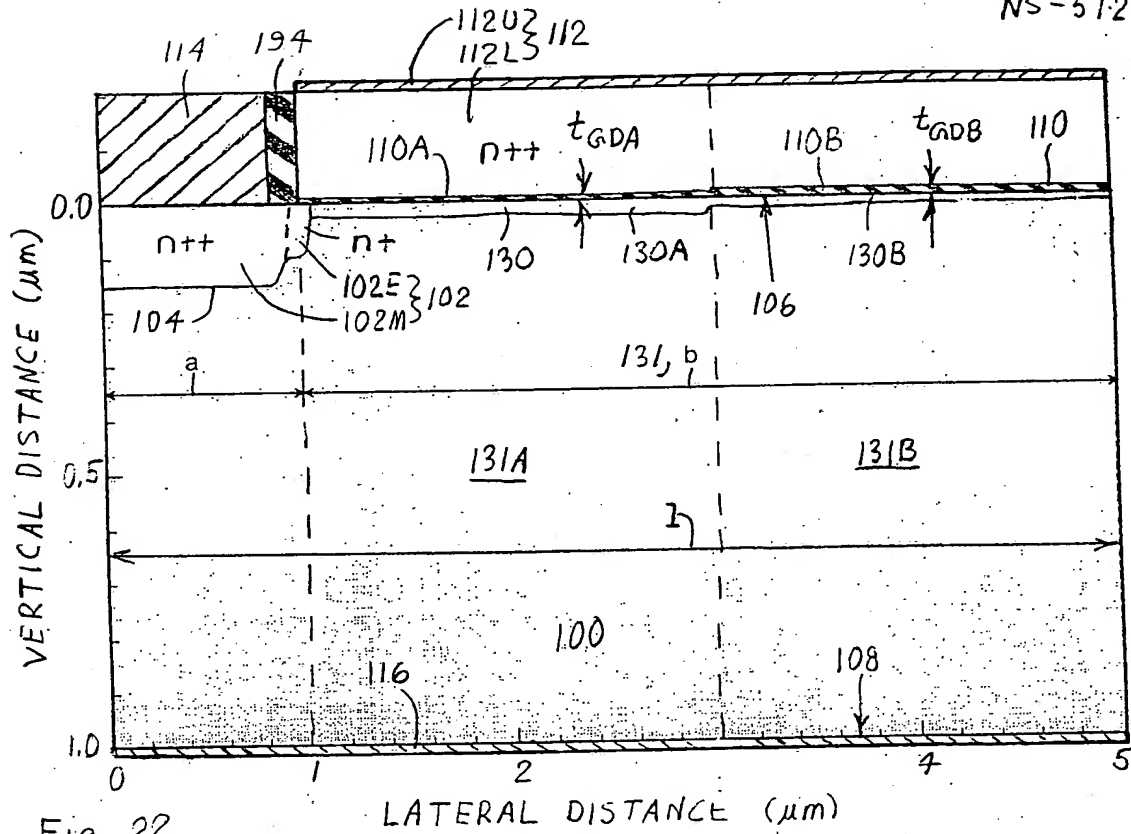


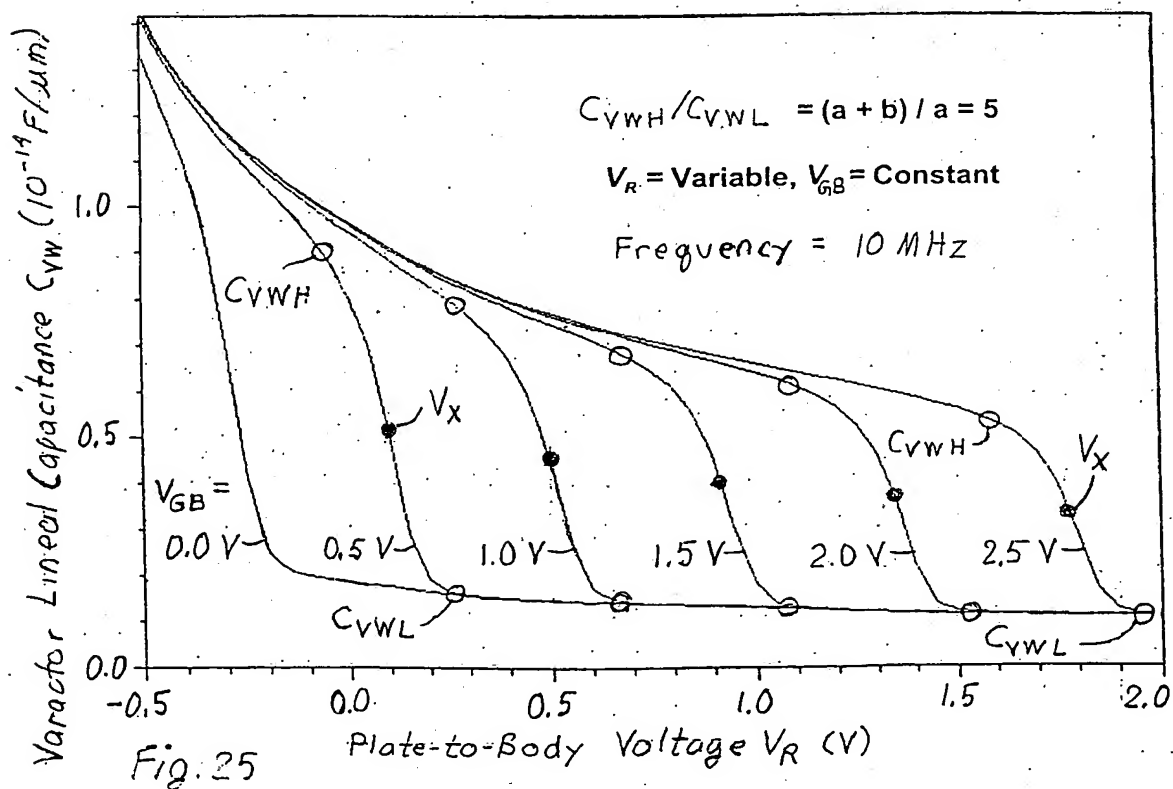
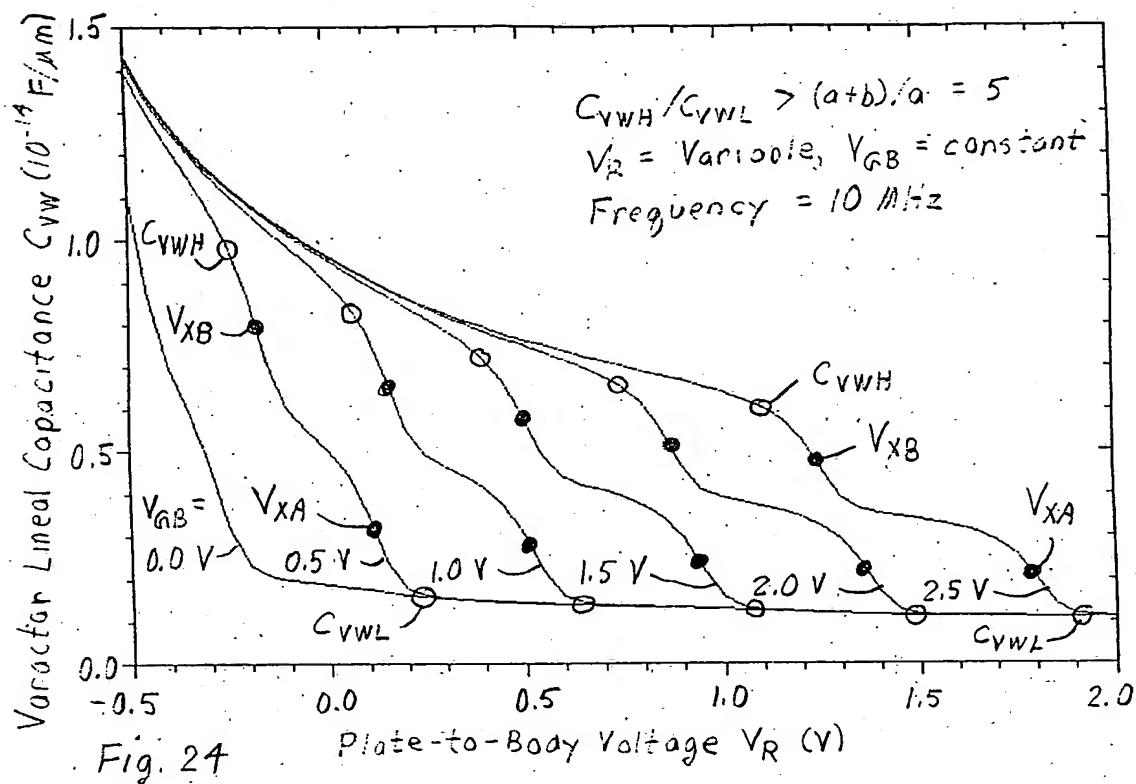
Fig. 21

NS-5127

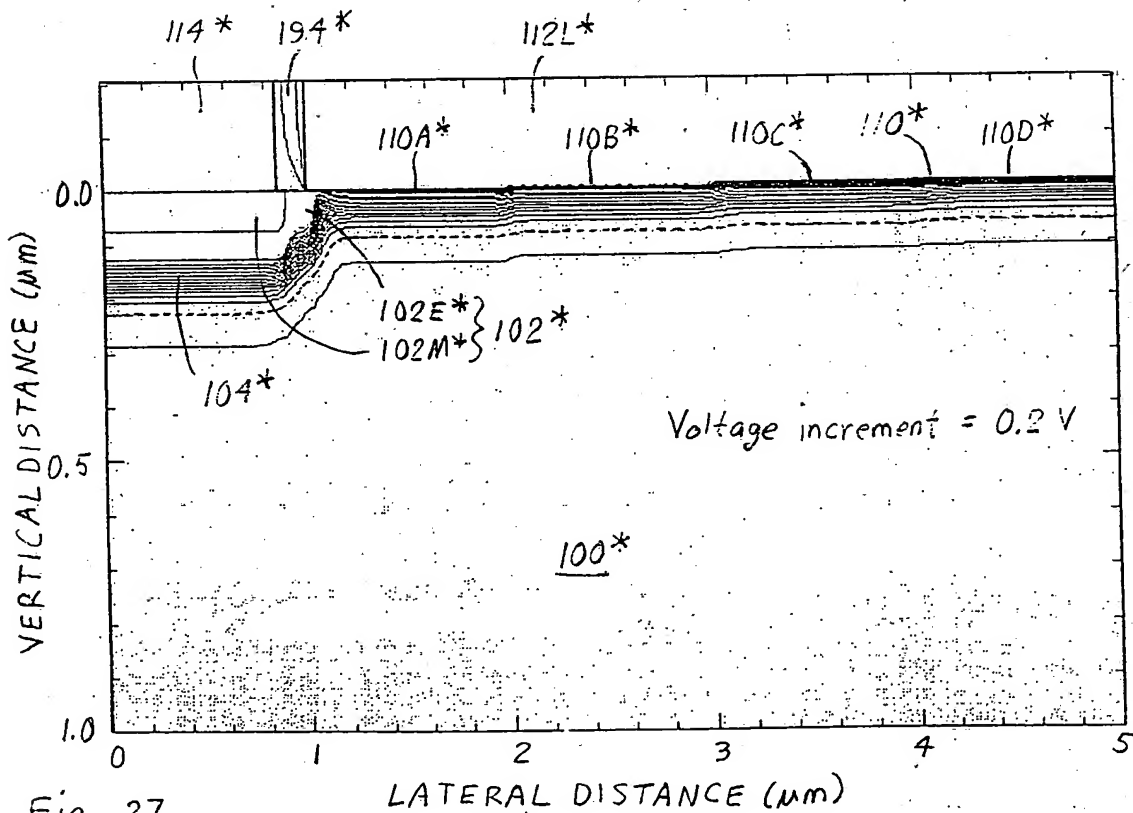
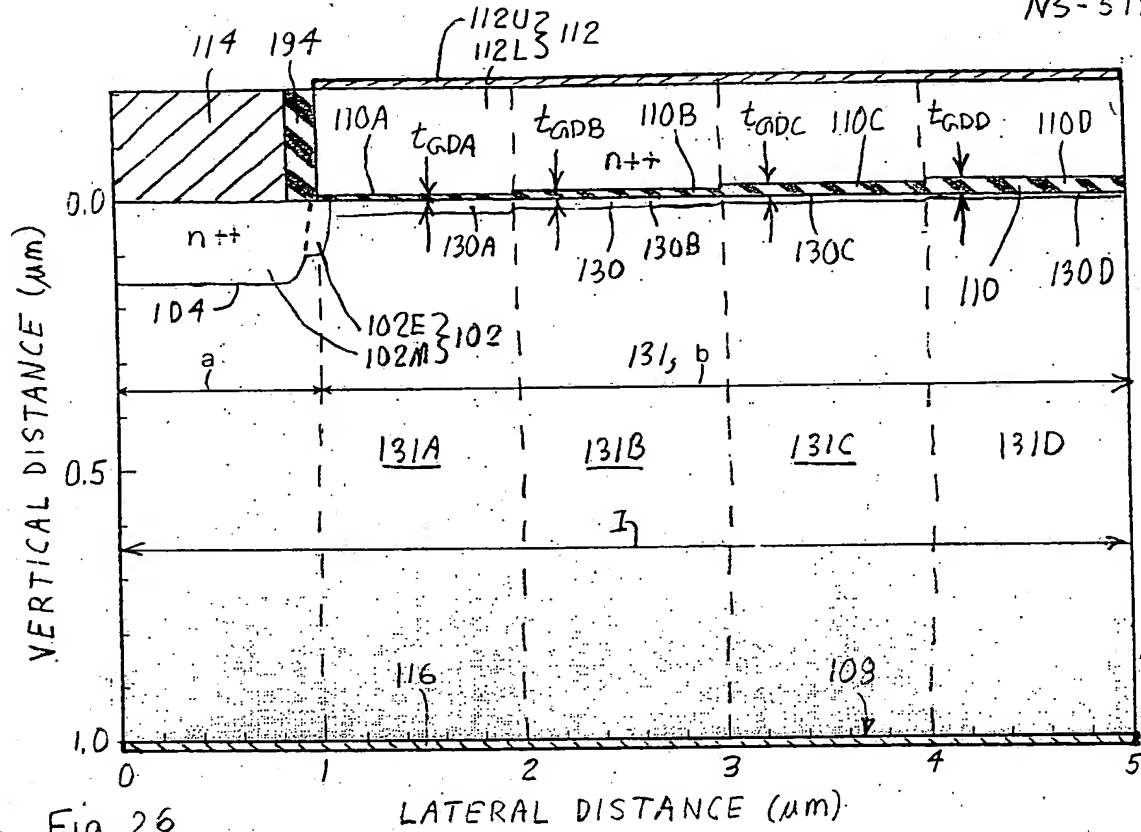


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NS-5127



NS-5127

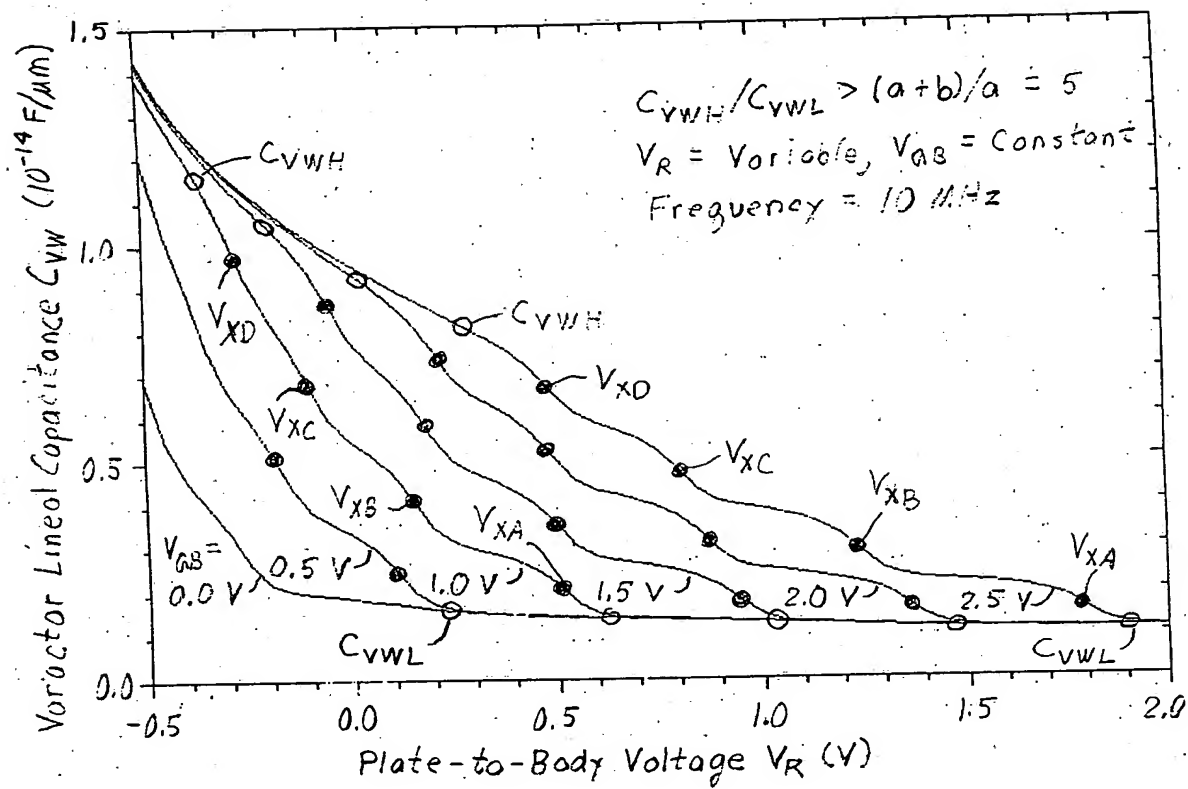


Fig. 28

Fig. 29

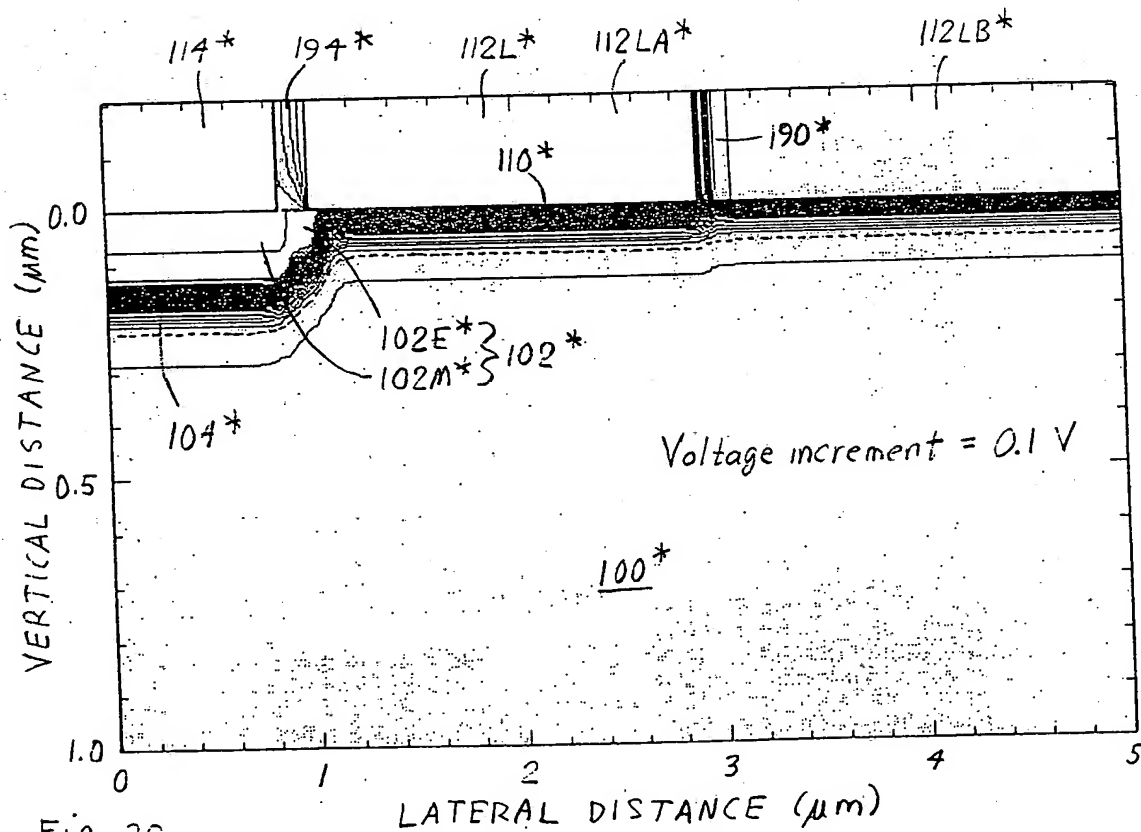


Fig. 30

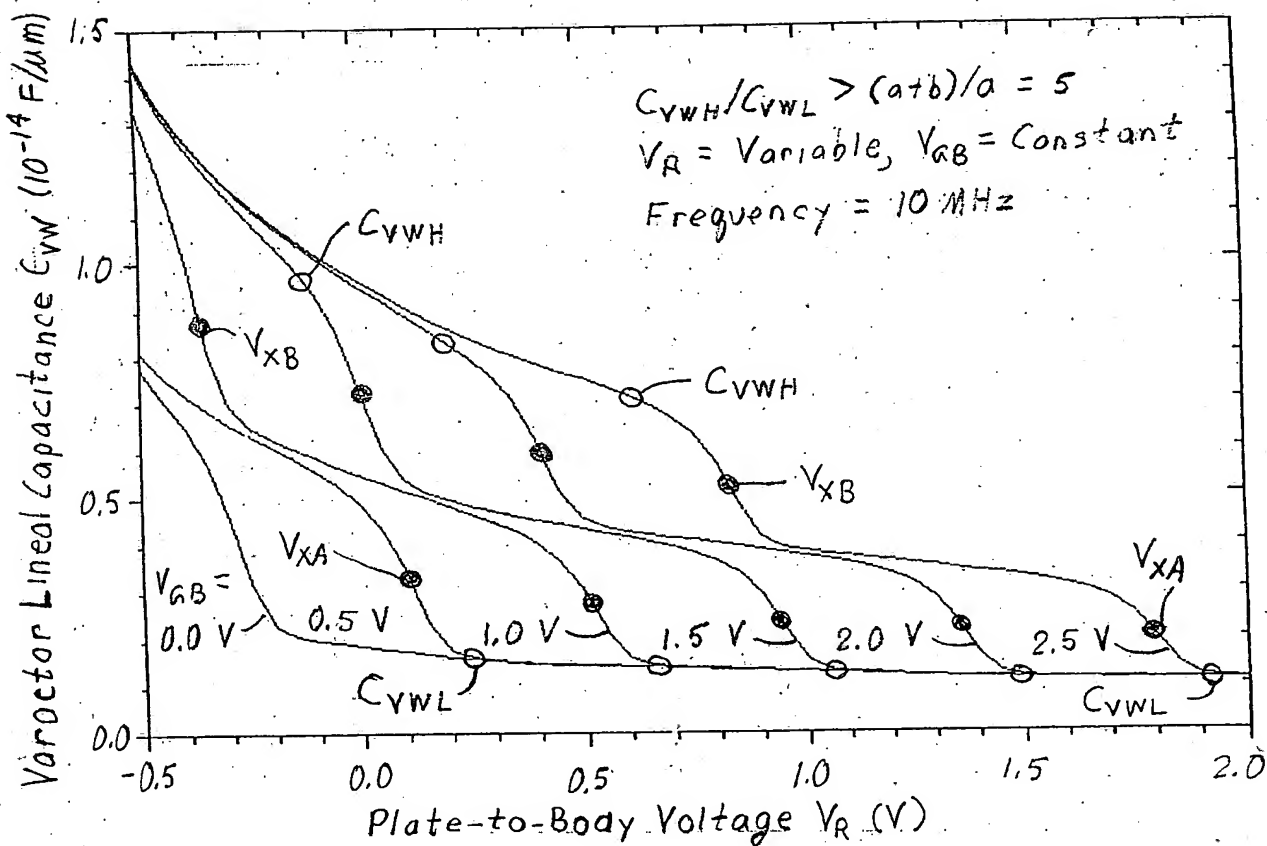


Fig. 31

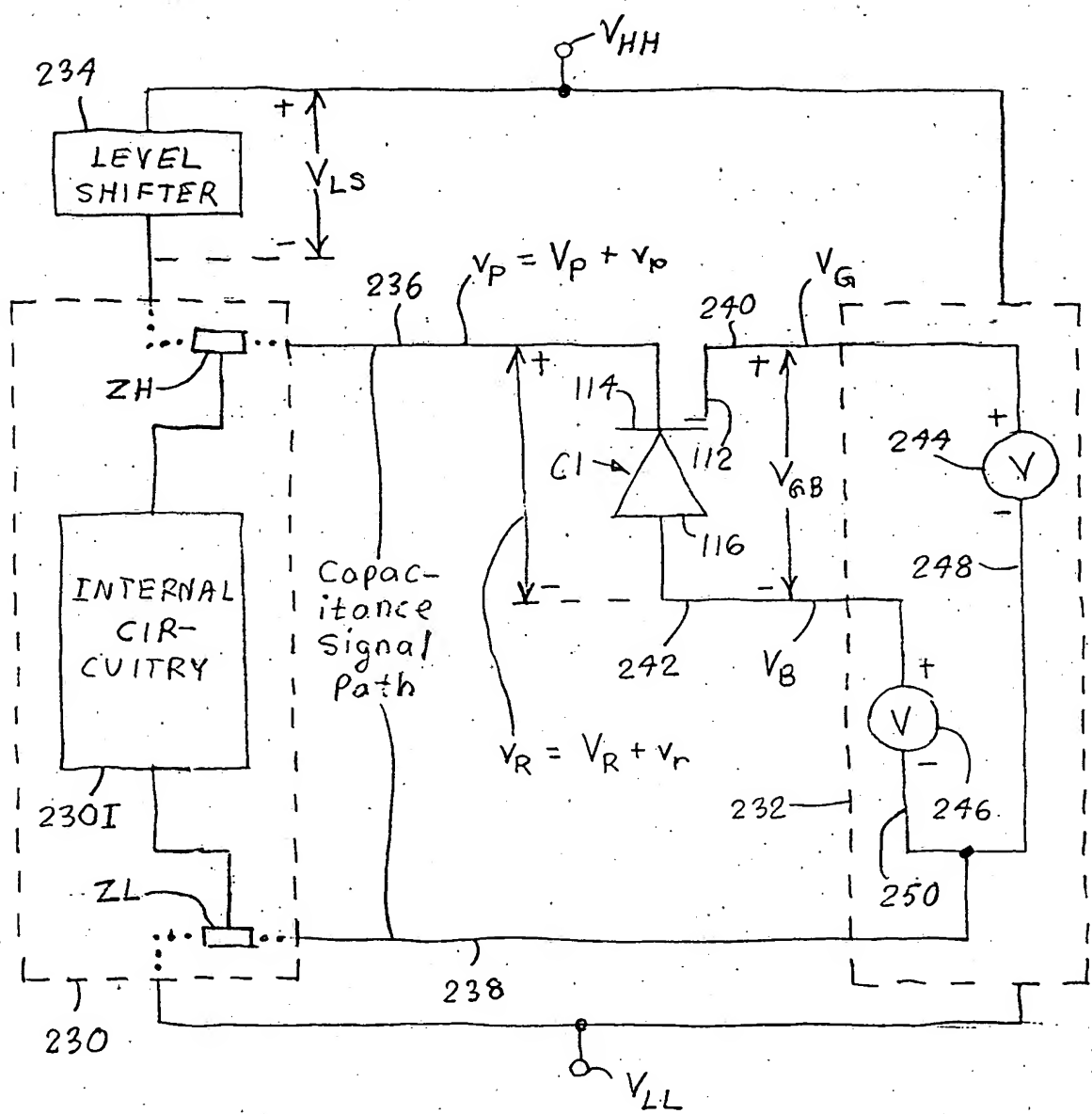


Fig. 32

Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Bulucea NS-5127 US
Sheet 2 of 26

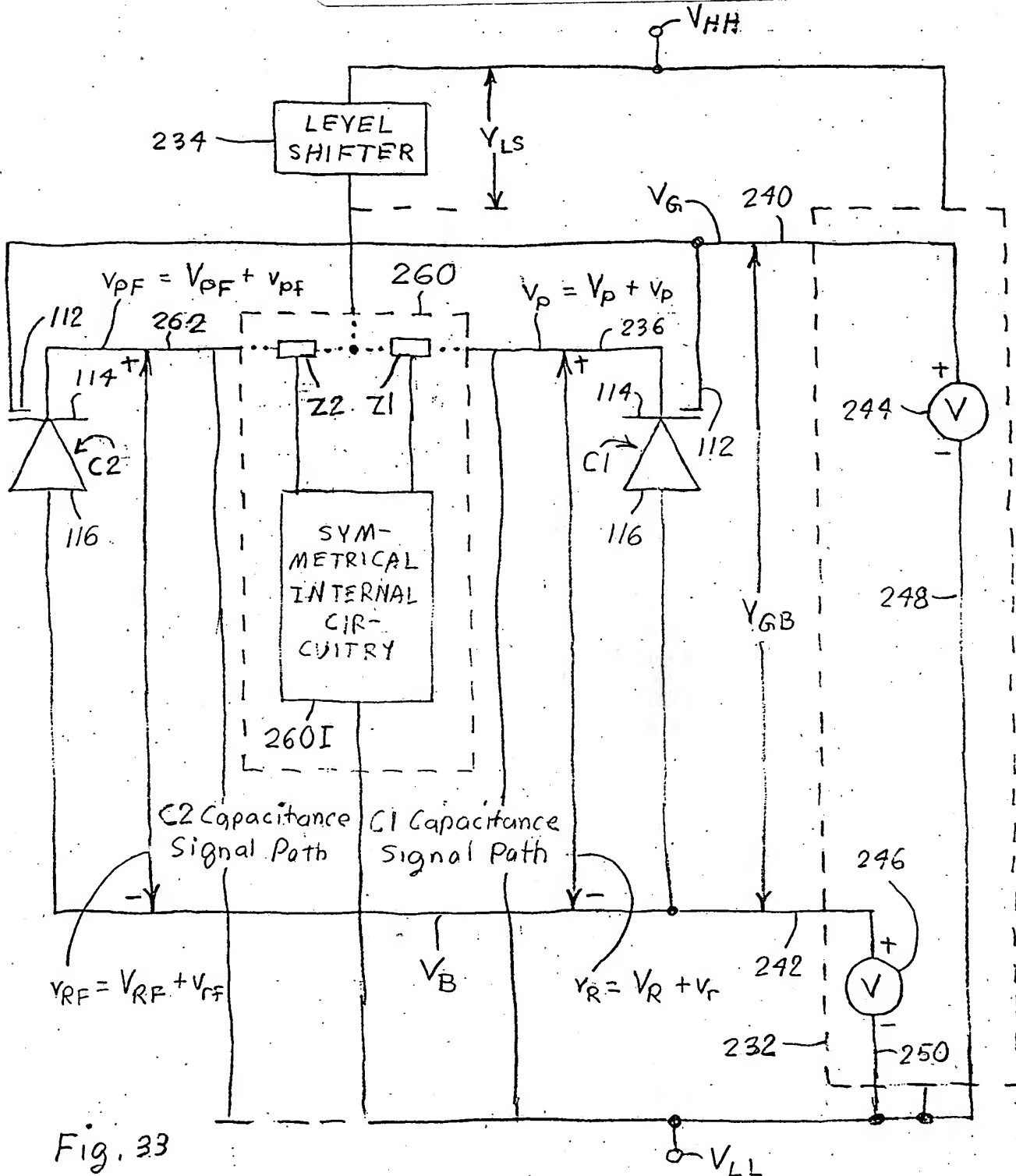


Fig. 33

Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Bulucea NS-5127 US
Sheet 2 of 26

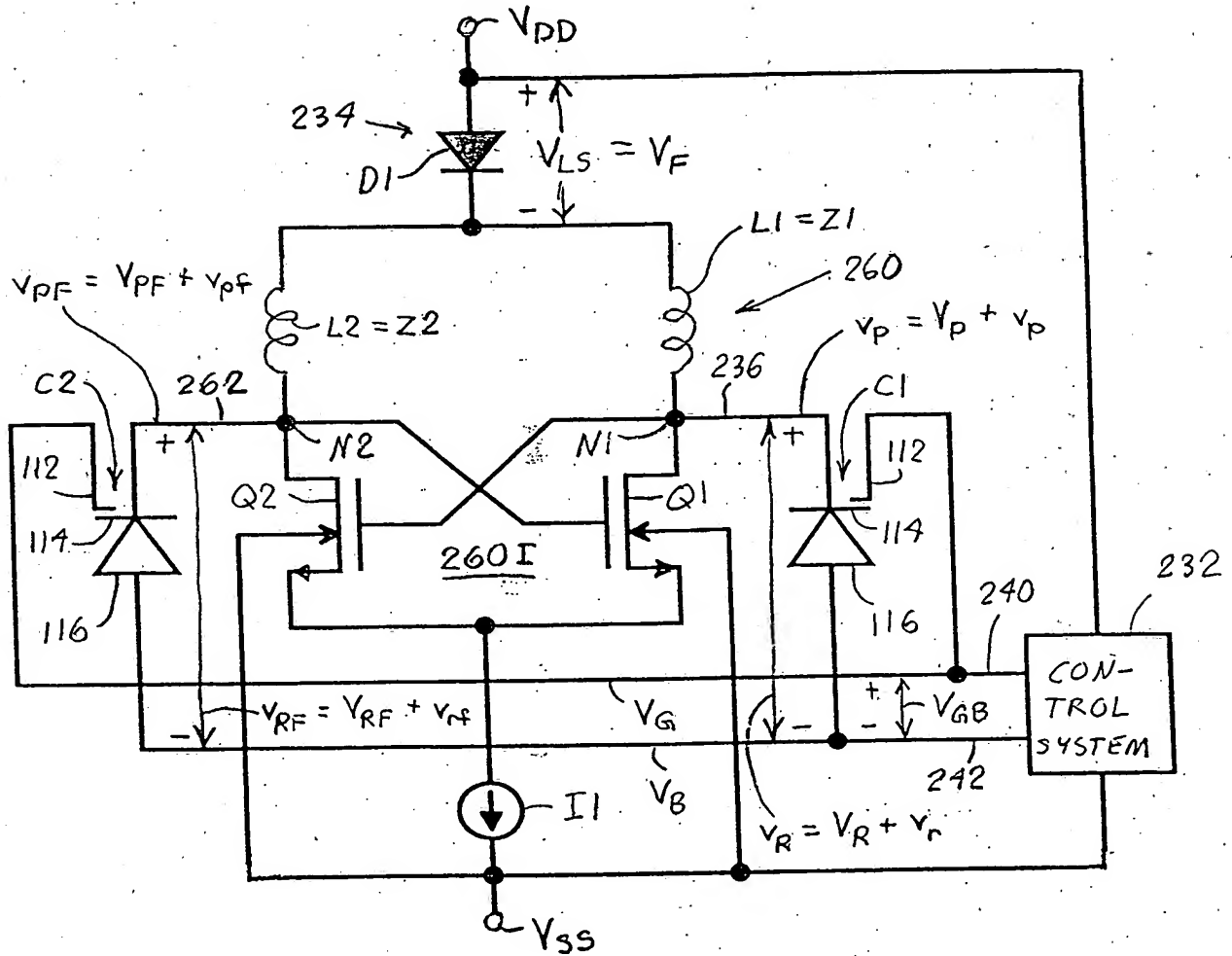


Fig. 34b

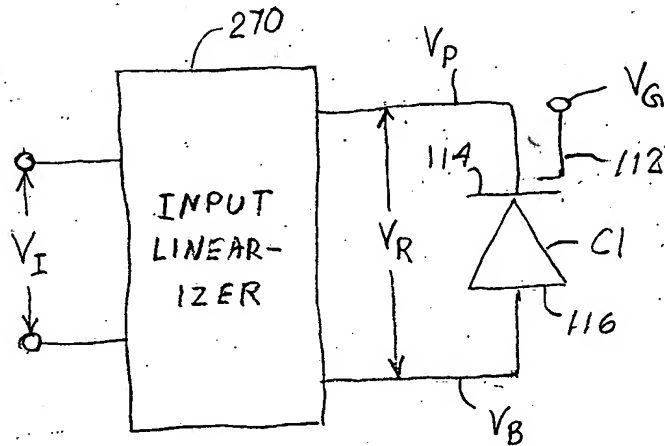


Fig. 35

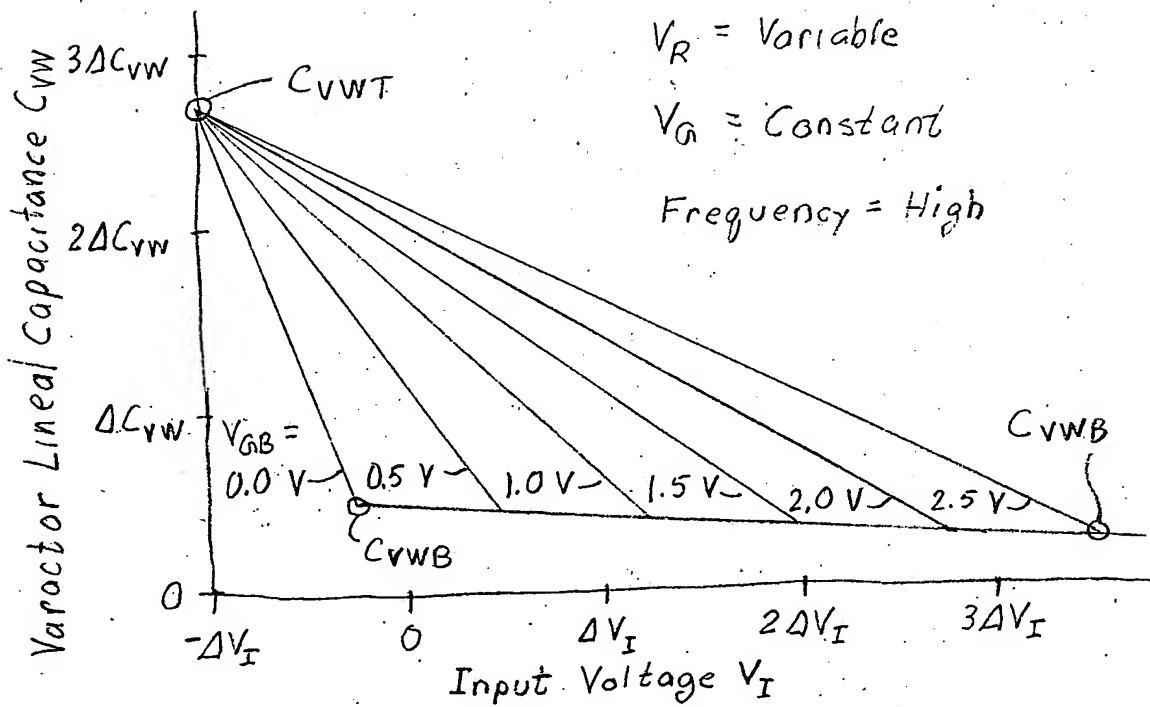


Fig. 36

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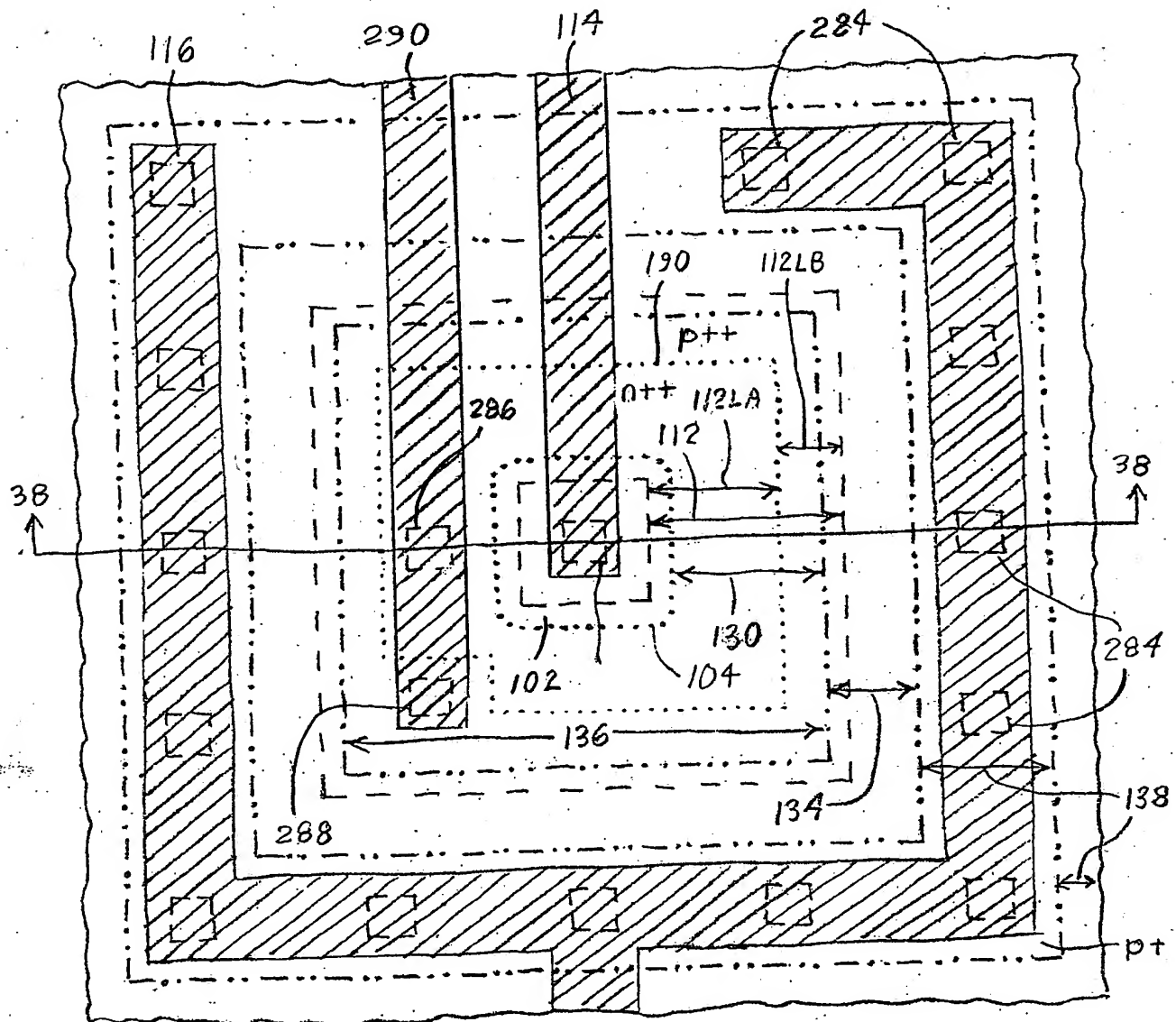


Fig. 37

